



ELECTRICAL SPECIFICATION

Parameter	Min.	Max.	Unit
	3.3		V
Supply Voltage Variation(V _{DD}) 10%	2.97	3.63	V
Frequency Range	60	200	MHz
Operating Temp. Range	Refer to Ordering Information		°C
Frequency Stability *	Refer to Ordering Information		ppm
Pulling Range	±100	–	ppm
Control Voltage Range	0.3	3.0	V
Supply Current			
60MHz ≤ Fo < 160MHz	–	40	mA
160MHz ≤ Fo ≤ 200MHz	–	50	
Output Level (CMOS)			
Output High (Logic “1”)	90% V _{DD}	–	V
Output Low (Logic “0”)	–	10% V _{DD}	
Transition Time:Rise/Fall Time ⁺			
60MHz ≤ Fo ≤ 200MHz	–	5	nSec
Start Time	–	5	mSec
Tri-State (Input to Pin 2)			
Output Active	2.0	–	V
Output in High Impedance State	–	0.5	
Absolute Clock Period Jitter	–	150	pSec
Linearity	–	10	%
Modulation Bandwidth	25	–	KHz
Input Impedance	2	–	MΩ
Storage Temp. Range	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

* Inclusive of calibration @ 25°C, operating temperature range, input voltage variation, load variation, aging, shock, and vibration.

⁺ Transition times are measured between 10% and 90% of V_{DD}, with an output load of 15pF.

