

**ELECTRICAL SPECIFICATION**

Parameter	Min.		Max.		Unit
	5.0	2.8	5.0	2.8	
Supply Voltage Variation(V_{DD}) 5%	4.75	2.66	5.25	2.94	V
Frequency Range (for TTL/CMOS output)	1.25		36		MHz
Frequency Range (for Clipped sine output)	10		36		
Operating Temp. Range	Refer to Ordering Information				°C
Frequency Stability	Refer to Ordering Information				ppm
Frequency Stability					
Vs Supply Voltage (±5%) change	-		±0.2		ppm
Vs Load (±10%) change	-		±0.2		
Vs Aging	-		±1.0		ppm / year
Supply Current (for TTL/CMOS output)					
1.2500MHz ≤ Fo < 10.000MHz	-		10	7	mA
10.000MHz ≤ Fo < 15.000MHz	-		15	10	
15.000MHz ≤ Fo < 26.000MHz	-		20	15	
26.000MHz ≤ Fo < 36.000MHz	-		25	20	
Supply Current (for Clipped Sine output)					
10.000MHz ≤ Fo < 15.000MHz	-		1.5		mA
15.000MHz ≤ Fo < 26.000MHz	-		2.0		
26.000MHz ≤ Fo < 36.000MHz	-		2.5		
Output Level (TTL/CMOS output)					
Output High (Logic "1")	90% V _{DD} or 2.4V		-		V
Output Low (Logic "0")	-		10% V _{DD} or 0.4V		
Duty	40%		60%		%
Output Level (for Clipped Sine output)	0.8		-		Vp-p
Vc Input Impedance	100		-		KΩ
Phase Noise @13.0MHz					
100Hz			-115		dBc/Hz
1KHz			-135		
10KHz			-148		
Start Time	-		2		mSec
Storage Temp. Range	-55		125		°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

