



ELECTRICAL SPECIFICATION

Parameter	Min.			Max.			Unit
	3.3	2.5	1.8	3.3	2.5	1.8	
Supply Voltage Variation(V _{DD}) 10%	2.97	2.25	1.62	3.63	2.75	1.98	V
Frequency Range	1			40			MHz
Standard Frequency	4.0, 6.0, 8.0, 12.0, 13.0, 16.0, 19.2, 20, 24.0, 26.0, 32, 38.4, 40						MHz
Operating Temp. Range	Refer to Ordering Information						°C
Supply Current	-			15	10	7	mA
Output Level (CMOS)							
Output High (Logic "1")	90% V _{DD}			-			V
Output Low (Logic "0")	-			10% V _{DD}			
Transition Time:Rise/Fall Time⁺							
1MHz ≤ Fo < 20MHz	-			6	8	8	nSec
20MHz ≤ Fo < 40MHz	-			5	6	6	
Start Time	-			5			mSec
Tri-State (Input to Pin 1)							
Output Active	0.7 V _{DD}			-			V
Output in High Impedance State	-			0.3 V _{DD}			
Absolute Clock Period Jitter	-			40			pSec
Standby Current	-			10			μA
Storage Temp. Range	-55			125			°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

* Inclusive of calibration @ 25°C, operating temperature range, input voltage variation, load variation, aging, shock, and vibration.

+ Transition times are measured between 10% and 90% of V_{DD}, with an output load of 15pF.