



**ELECTRICAL SPECIFICATION**

Parameter	Min.				Max.				Unit
	5.0	3.3	2.5	1.8	5.0	3.3	2.5	1.8	
<b>Supply Voltage Variation(V<sub>DD</sub>) 10%</b>	4.5	2.97	2.25	1.62	5.5	3.63	2.75	1.98	V
<b>Frequency Range</b>	0.032768		0.3456	0.6912	75		125		MHz
<b>Operating Temp. Range</b>	Refer to Ordering Information								°C
<b>Frequency Stability *</b>	Refer to Ordering Information								ppm
<b>Supply Current</b>									
0.032768MHz ≤ Fo < 0.6912MHz	-				7	5	5	-	mA
0.6912MHz ≤ Fo < 1.5MHz	-				7	5	5	5	
1.5MHz ≤ Fo < 20MHz	-				10	7	7	5	
20MHz ≤ Fo < 50MHz	-				30	20	15	15	
50MHz ≤ Fo < 70MHz	-				40	30	20	15	
70MHz ≤ Fo ≤ 75MHz	-				40	30	20	20	
75MHz < Fo < 100MHz	-				-	30	25	20	
100MHz ≤ Fo	-				-	40	30	25	
<b>Output Level (CMOS)</b>									
Output High (Logic "1")	90% V <sub>DD</sub>				-				V
Output Low (Logic "0")	-				10% V <sub>DD</sub>				
<b>Transition Time:Rise/Fall Time*</b>									
0.032768MHz ≤ Fo < 0.3456MHz	-				200	200	200	-	nSec
0.3456MHz ≤ Fo < 0.6912MHz	-				8	10	10	-	
0.6912MHz ≤ Fo < 20MHz	-				8	10	10	10	
20MHz ≤ Fo < 50MHz	-				5	6	6	6	
50MHz ≤ Fo < 70MHz	-				2	3	3	3	
70MHz ≤ Fo ≤ 75MHz	-				2	3	3	3	
75MHz < Fo	-				-	3	3	3	
<b>Start Time</b>	-				8				mSec
<b>Tri-State (Input to Pin 1)</b>									
Output Active	4.0	2.0	1.75	1.26	-				V
Output in High Impedance State	-				0.8	0.5	0.5	0.5	
<b>Absolute Clock Period Jitter</b>	-				40				pSec
<b>Standby Current</b>	-				10				µA
<b>Storage Temp. Range</b>	-55				125				°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

\* Inclusive of calibration @ 25°C, operating temperature range, input voltage variation, load variation, aging, shock, and vibration.

\* Transition times are measured between 10% and 90% of V<sub>DD</sub>, with an output load of 15pF.

