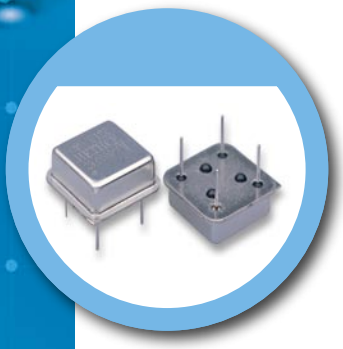
RoHS Compliant Optional

OH Type Crystal Oscillator



FEATURE

1. Typical 12.8 × 12.8 × 5.0 mm standard package.
2. Compatible with 8-Pin dual in line.
3. CMOS circuit TTL/CMOS compatible.
4. Hermetically sealed metal case and high reliability.
5. Tight symmetry (45 to 55%) available.
6. Packing: 50pcs per Tray.

ORDERING INFORMATION

Select option

XO	Package (mm)	Supply Voltage(V)	Tri-State Function	Freq. Stability (ppm)	Temp. Range (°C)	Output LOgic and Symmetry	Oscillator Mode	Appearance	Lead Free	Dash	Freq. (MHz)
	12.8×12.8	Through Hole T : 5 E : 2.8 ~ 3.3 J : 2.5 K : 1.8 Gull Wing G : 5 F : 2.8 ~ 3.3	T : Fixed-Freq with Tri-State	C : ± 20 D : ± 25 G : ± 50 H : ±100	C : -20~+70 D : -30~+80 L : -40~+85	50±5% TTL A TTL 50pF E CMOS 15pF J CMOS 50pF F	-A : AT Fundamental -T : AT 3rd Overtone NOT SELECTABLE BY CUSTOMER	N : Normal	F : RoHS Compliant L : Not RoHS Compliant		XX.XXXXXX

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Example OHTTDCJANF-14.318180

XO H-TYPE; VDD: 5V; Fixed-Freq. with Tri-State; Freq. Stability: ±25ppm; Temp. Range: -20ºC to +70ºC; Load: CMOS 15pF, Symmetry: 50±5%; AT Fundamental; Normal Appearance; RoHS Compliant; Freq. 14.318180MHz.

* Not all combinations of options are available.

ELECTRICAL SPECIFICATION

Parameter	5V±10%	3.3V±10%
Frequency Range (MHz)	1~99	1~125
Operating Temp. Range (°C)	Refer to Ordering Information	
Frequency Stability *	Refer to Ordering Information	
Supply Current (mA) max.		
1.0MHz ≦ Fo <20MHz	15	10
20MHz ≦ Fo <50MHz	40	30
50MHz ≦ Fo	50	40
Transition Time :Rise/Fall Time (ns) Max.+		
1.0MHz ≦ Fo <20MHz	8	10
20MHz ≦ Fo <50MHz	5	6
50MHz ≦ Fo	4	5
Storage Temp. Range (°C)	-55~+125	

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C) \ ppm		D: ±25	G: ±50	H: ±100
C	-20~ +70	○	○	○
D	-30~ +80	○	○	○
L	-40~ +85	○	○	○

○:Standard

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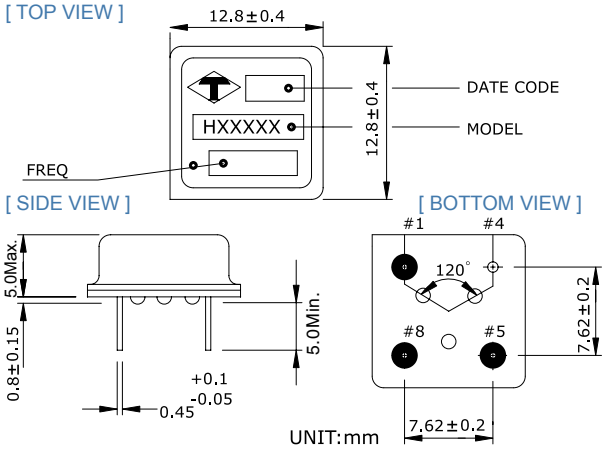
Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

* Inclusive of calibration @ 25ºC, operating temperature range, input voltage variation, load variation, aging, shock, and vibration.

+ Transition times are measured between 10% and 90% of VDD, with an output load of 15pF.

OUTLINE DRAWING



Pin	Function
#1	Tri-State
#4	CASE GND
#5	Output
#8	VDD

Specifications subject to change without notice.