



**ELECTRICAL SPECIFICATION**

Parameter					
	2.5	3.3	2.5	3.3	V
<b>Supply Voltage Variation(V<sub>DD</sub>) 10%</b>	2.25	2.97	2.75	3.63	V
<b>Frequency Range</b>	4.75		54		MHz
<b>Operating Temp. Range</b>	Refer to Ordering Information				°C
<b>Frequency Stability *</b>	Refer to Ordering Information				ppm
<b>Supply Current</b>					
4.75MHz ≤ Fo < 20MHz	-		2	3	mA
20MHz ≤ Fo < 40MHz	-		3.5	5	
40MHz ≤ Fo ≤ 54MHz	-		5	7	
<b>Output Level (CMOS)</b>					
Output High (Logic "1")	90% V <sub>DD</sub>		-		V
Output Low (Logic "0")	-		10% V <sub>DD</sub>		
<b>Transition Time : Rise /Fall Time +</b>	6				nSec
<b>Start Time</b>	8				mSec
<b>Tri-State</b>					
Output Active	1.75	2.0	-		V
Output in High Impedance State	-		0.5	0.5	
<b>Absolute Clock Period Jitter</b>	-		40		pSec
<b>Standby Current</b>	-		5		μA
<b>Storage Temp. Range</b>	-55		125		°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

\* Inclusive of calibration @ 25°C, operating temperature range, input voltage variation, load variation, aging, shock, and vibration.

+ Transition times are measured between 10% and 90% of V<sub>DD</sub>, with an output load of 15pF.